Archit Jain

ID: 200401124 Date of Birth: 02-08-1984 B. Tech (ICT)

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Education			
Standard	University/School	Year	Percentage/CPI
B. Tech (ICT)	DA-IICT	Pursuing	8.2/10.0 (Till 6 th Semester)
AISSCE(XII th)	BGKV, Kolkata	2002	84.5%
AISCE (X th)	Central Academy, Udaipur	2000	89.5%
Skills			
Prog. Languages	Imperative: C, Java, Microsoft Visual C#.net, Python Functional: Haskell Logical: Prolog Hardware: Verilog , 8085 Microprocessor Assembly Level language		
Operating System	MS DOS, Windows 3.01 95, 98 XP, Vista, Ubuntu 7.01, Mac OS X 10.4.8		
Tools	Latex, LTSpice, PSpice, , Rational Suite, LC-3 Simulator, 8085 simulator, Macromedia Dreamweaver MX, Scilab ,Matlab, Net Beans Ethereal (Packet		
	Sniffer), GNU GCC Debugger, Yacc, Visual Prolog, Glasgow Haskell		
Database Test	Compiler		
Database Tools	My SQL, IBM DB2		

Elective Courses Taken

Technical : Introduction to VLSI, Digital Systems and Architecture, Pattern Recognition, Programming Languages.

Open : Truth Fiction & Cyber Space , Approaches to Science Fiction.

Projects / Papers Presented

- 1. RSS News Reader and Search Engine (Guide: Prof. Sanjay Chaudhry) Created a local RSS Sever by using Pyhton Script and parsed the RSS Feed using XML .The results were stored in a DBMS and Search was done on the same.
- 2. **Smart Intelligent Diaries** A system which took various medical parameters as input and gave important alerts to the user so that the overall health of the user could be effectively monitored. Presented : Microsoft Imagine Cup 2006 Cleared Level 1 3. Vibhava (Guide: Prof. Saniav Chaudhrv) An embedded device which is an effective replacement of input devices like keyboard, mouse, phone pads etc. Presented : Microsoft Imagine Cup 2007 Awaiting Level 2 Result.Cleared Level 1 Emergency Management System (Guide: Prof. Anutosh Maitra) 4. Automation of the work of police dept. and fire dept. by making a generic framework in which they can effectively manage emergencies. 5. Placement Management System (Guide: Prof.Asim Banerjee) To aid in the placement process of our institute and to make the whole process more efficient we made the current placement system which is deployed for our placements. Automated Testing Tools were used.
- Role: Team Leader
 2D Discrete Cosine Transform (Guide: Prof. Rahul Dubey) An FPGA Implementation of 2D DCT Transform which is used for image compression.

- 7. Predictive Dialer Optimization using Genetic Algorithms (Guide: Prof. R.B.Lenin) Presented a paper in the Mindtree's Tech fest Osmosis 2006. The paper discussed the optimization strategy of a predictive dialer which is used in call centers to reduce wait times.
- 8. **MyCompiler** (Guide: Prof. Kalpesh Kapoor) Hand coded a primitive compiler for a language of my own. The compiler was then automatically generated using Yacc.
- **Co-reference Resolution** 9. (Guide: Abhinaya Pandya) Rule based A.I. engine for natural language processing was made. The system had achieved 89.7% efficiency in resolving co – reference ambiguities.
- 10. Binary to decimal floating point converter in hardware Optimized Tansistor level design in LTspice for converting binary to decimal.
- 11. Science Fiction Term Paper (Guide: Prof. Aditi Nath Sarkar) Proved the existence of a universal observer in a term paper presented as a part of science fiction course

Internships

1. **Rural Internship**

> **Organization** Harijan Sewak Sangh in Howrah, West Bengal. & Place:

Description: Served as a computer instructor for rural kids and taught them basics of computers.

2. Industrial Internship

Organization Adiance Technologies, Ahmedabad & Place: Description: Made a self evolving algorithm for predictive dialer

3. **Research Internship**

Organization DA-IICT, Gandhinagar. & Place: Description: Working under Prof. Manish K Gupta in DNA self assembly and Tiling theory for making gray counter.

Personal Achievements

- Organized Catechize in Synapse 2007
- Organized Kite-Flying in Synapse 2005
- Served as a technical head of institute's Press Club
- Volunteered in the organization of NWCV 2006
- Served as a member of the CORE Volunteer Team of IEEE DA-IICT.
- Attended a short term course at IIT Kgp in VLSI Signal Processing.
- Learnt Basic French as a course flouted by Alliance Francis
- Yellow Belt in Karate

Work Aspiration

- DNA self assembly and computation.
- Pattern Recognition
- Massively Parallel Super Scalar Architectures
- Swarm Intelligence
- Natural Language Processing

Declaration: The above information is correct to the best of my knowledge.

Archit Jain.

